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Floating-point and multibit-increment digital-differential-analyser structures

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Title:	Floating-point and multibit-increment digital-differential-analyser structures
Year:	1972
Author:	<u>Philokyprou, G.</u> and <u>Halatsis, C.</u>
Abstract:	The possibilities of establishing digital-differential-analyser structures possessing both high solution accuracies and automatic scaling are examined. Two such structures are given, together with their performance characteristics. The second of these, the dynamic-increment digital differential analyser, introduces the concept of a digital differential analyser with the length of its incremental output dynamically varying during the solution process.